SWAMI VIVEKANANDA UNIVERSITY
## ELECTRONICS AND COMMUNICATION ENGINEERING

Two weeks Cadence Based PCB Design workshop (09.12.2024-21.12.2024)

A.Students gained hands-on experience in designing printed circuit boards (PCBs) using industry-standard Cadence tools. They learned schematic design, component placement, routing, and simulation.

B.This improved their practical skills in PCB design, making them industry-ready for hardware development roles. Improved understanding of signal integrity and real-world circuit challenges.

C. This workshop strengthened the department's reputation for VLSI and hardware design training and encouraged collaborative learning and innovation in circuit design.

D. It inspired students to take up PCB-based projects and interdisciplinary research, fostering a strong hardware development culture.
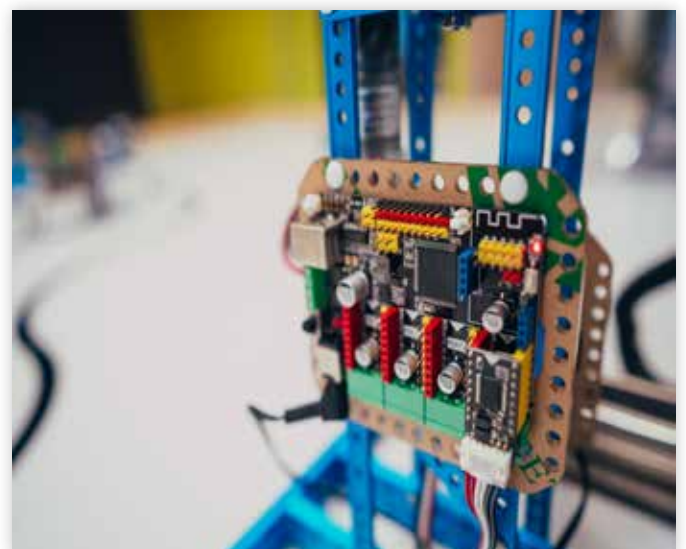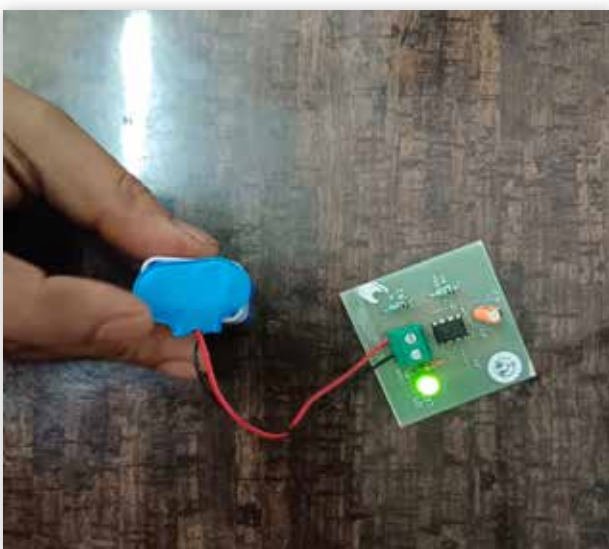
Project presentation for Final Year ECE Students

A.Students showcased innovative projects in areas like embedded systems, IoT, communication, and VLSI. They developed strong presentation skills and the ability to defend their technical works.

B.This improved their confidence, technical articulation, and problem-solving abilities. Gained valuable feedback from faculty and industry experts.

C.It highlighted the quality of student research and project-based learning and encouraged juniors to take up impactful projects.

Weekly training on IC Design and FPGA using Cadence tools

A. Students acquired a deep understanding of IC design, verification, and FPGA prototyping. They gained hands-on experience in RTL design, synthesis, and layout.

B. This developed industry-relevant skills, making them competitive for semiconductor and VLSI job opportunities. Increased proficiency in Cadence tools, crucial for chip design.

C. This positioned our department as a hub for advanced semiconductor training. Encouraged faculty and students to pursue research in IC design.

D. It helped to create a strong foundation for students interested in ASIC/FPGA design, reinforcing the department's focus on cutting-edge technology.